Methods and Apparatus for Early Loop Bottom Detection in Digital Signal Processors
Mayer, Christopher
Docket No.: A0312.70524US00

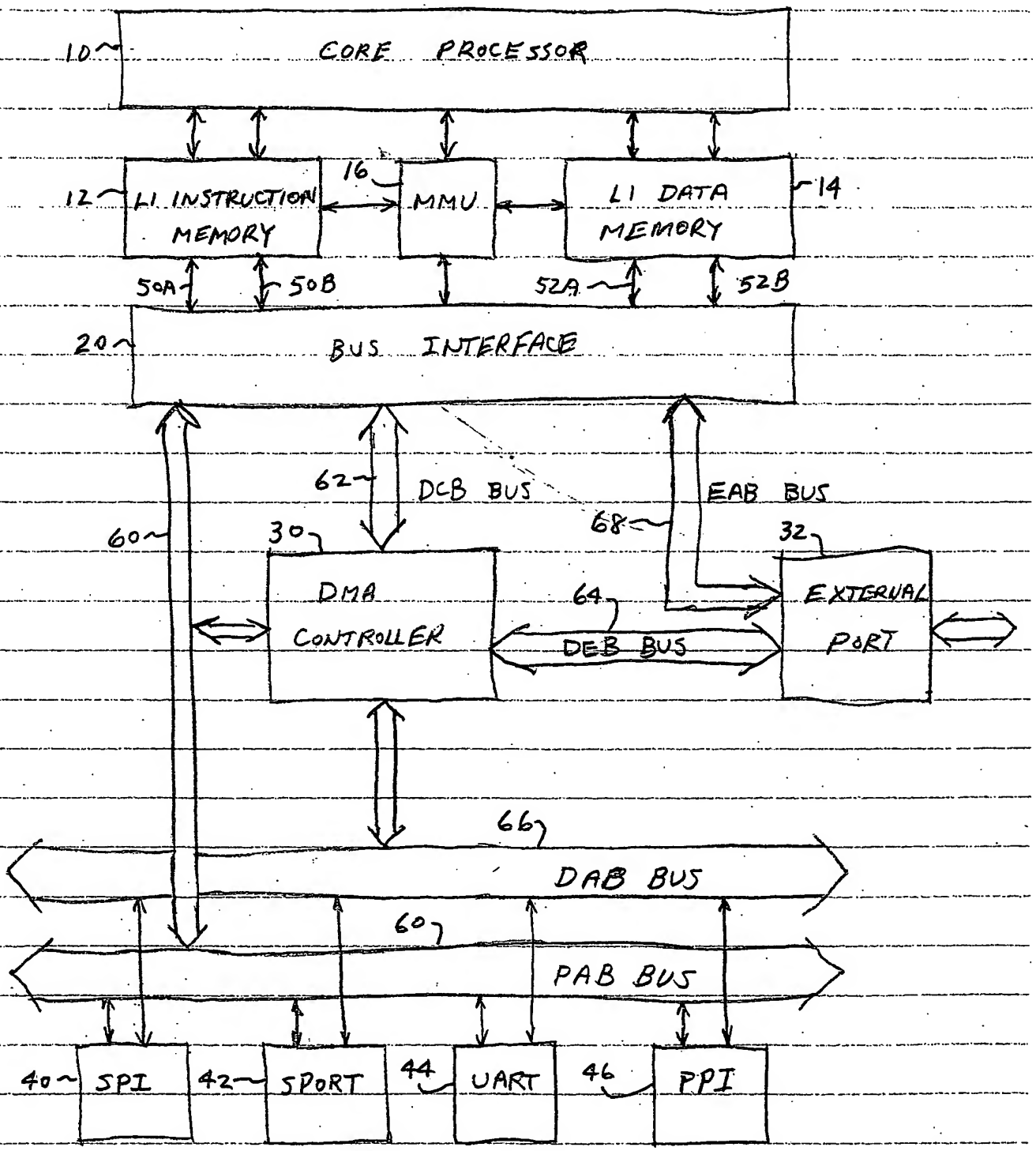

FIG. 1

BEST AVAILABLE COPY

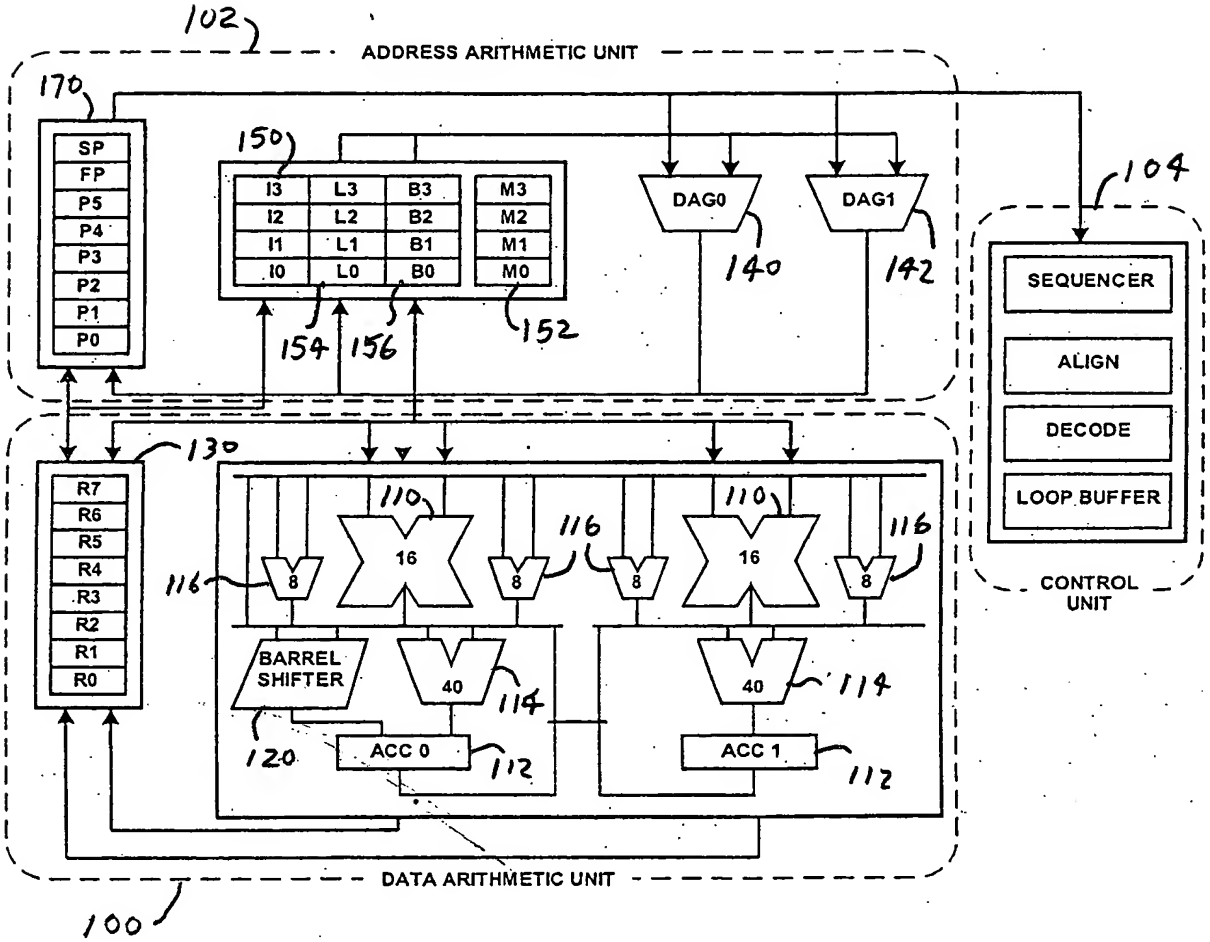

FIG. 2

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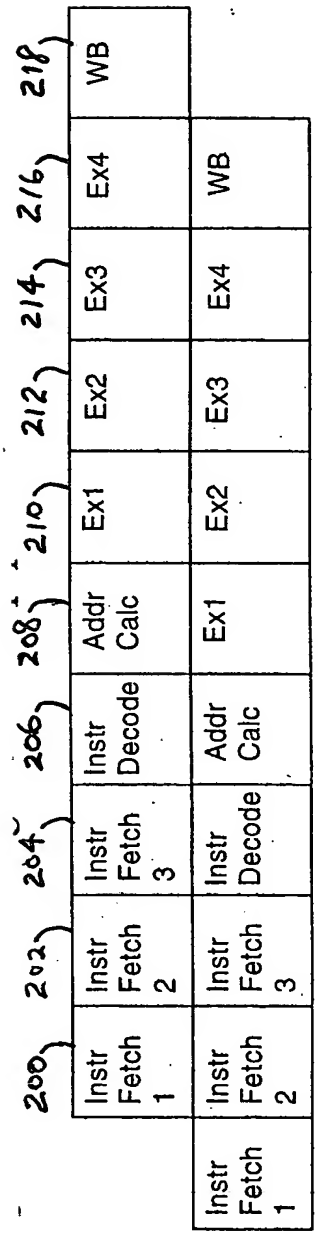

FIG. 3

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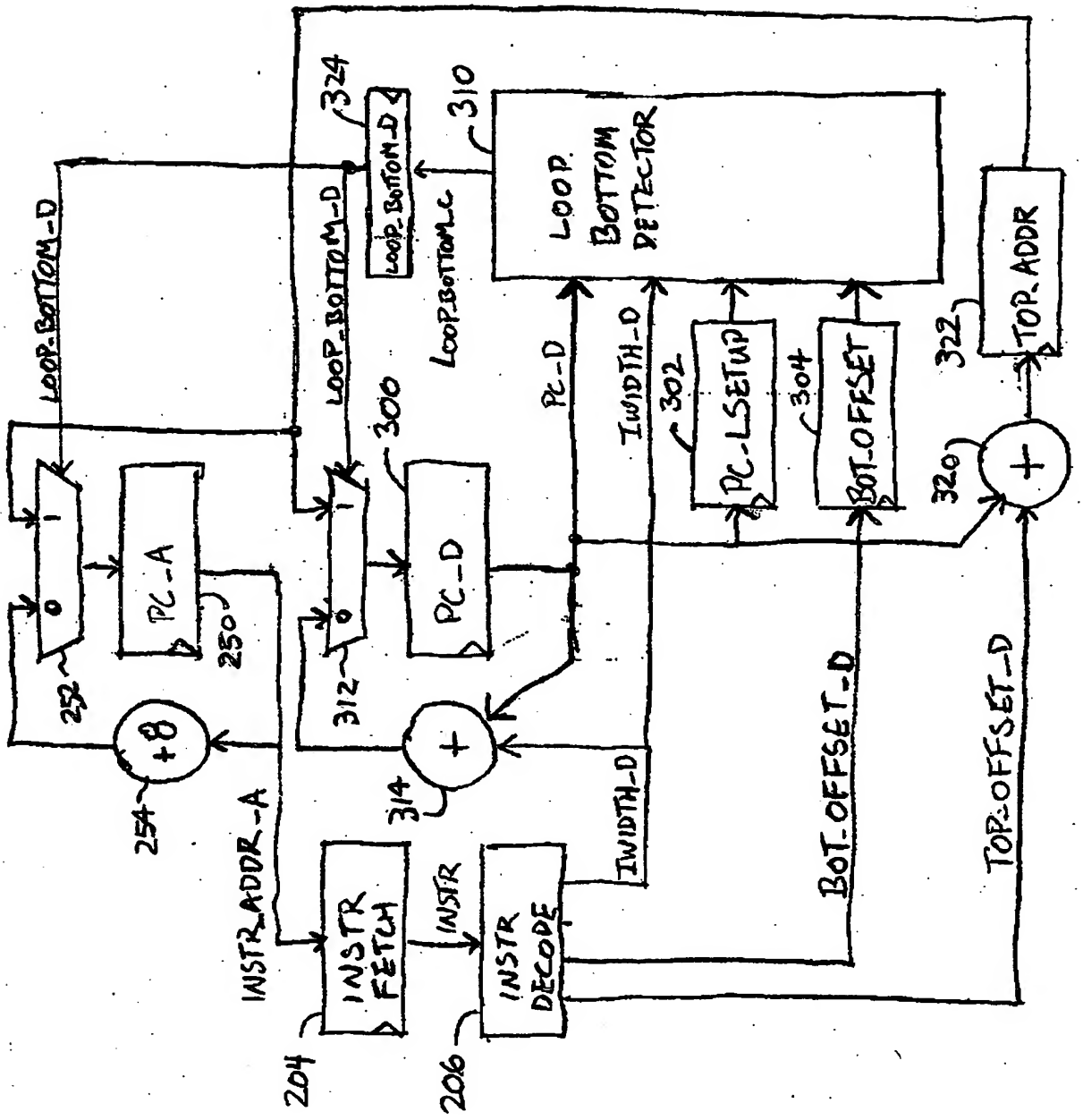

FIG. 4

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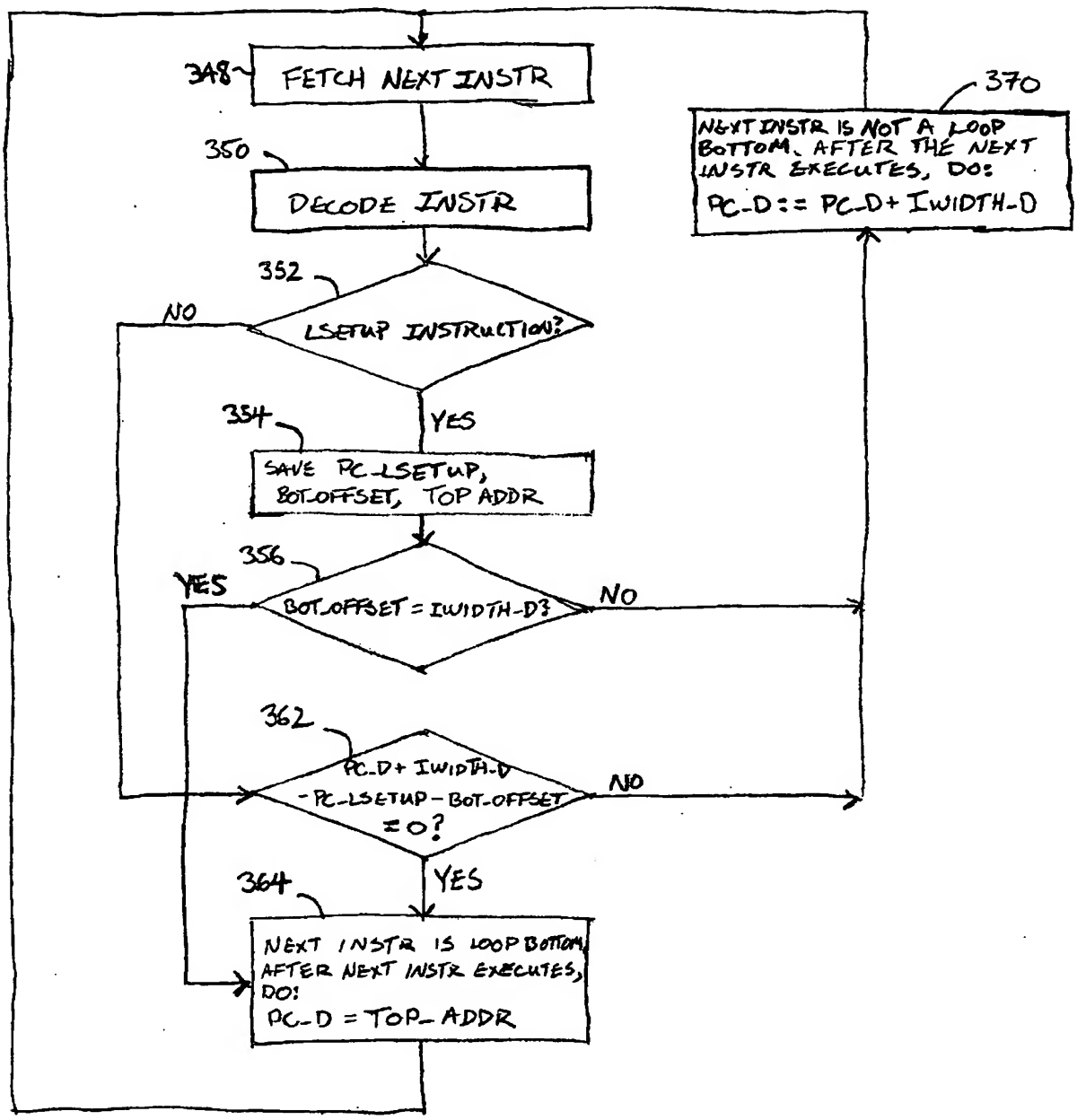

FIG. 5

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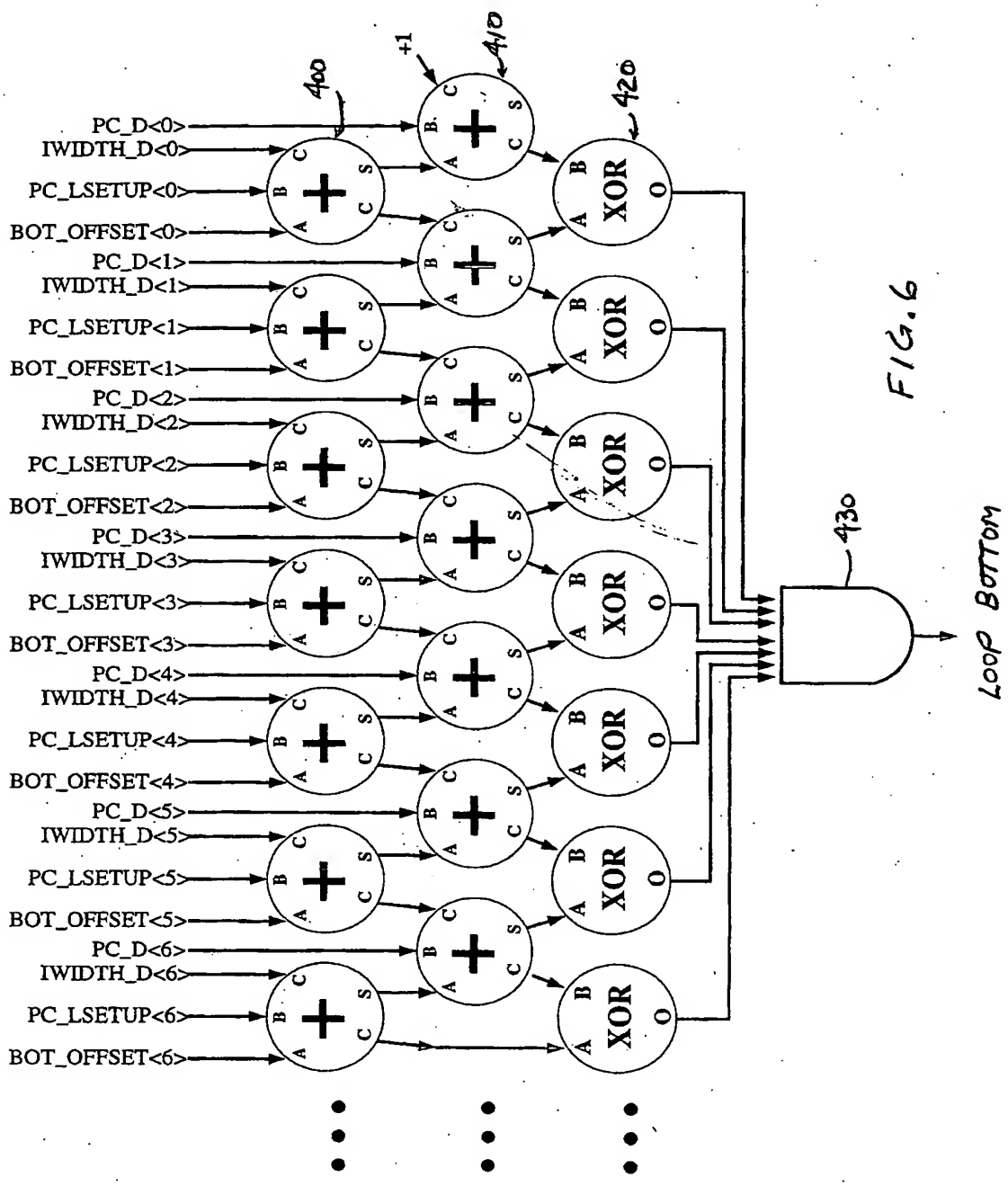

FIG. 6

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